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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

76	HAYASHI, Furnihiko and PLUMMER, James D., "A Self-Aligned Split-Gate Flash EEPROM Cell With 3-D Pillar Structure", 1999 Symposium on VLSI Technology Digest of Technical Papers, Center for Integrated System, Stanford University, Stanford, CA 94305, USA, pages 87-88

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Date Considered:

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